



501.40536X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): T. Fujiwara, et al.

Appln. No.: 09/913,896

Filed: August 21, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND PROCESS
FOR MANUFACTURING THE SAME

Group: 2811

Examiner: S. Gebremariam

RESPONSE TO NOTICE OF NON-COMPLAINT
AMENDMENT (37 CFR § 1.121)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 20, 2003

Sir:

In response to the Notice of Non Compliant Amendment (37 CFR § 1.121) dated October 20, 2003, Applicants resubmit herewith the "Amendments to the Specification" portion of the Amendment filed on September 11, 2003, including the paragraph numbers on the paragraphs being amended.

It is respectfully submitted that the amendments to the specification were to the Substitute Specification and not the original specification as indicated on the Notice of Non-Compliant.

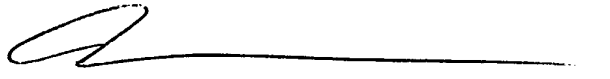
In view of the foregoing comments and amendments, reconsideration and allowance of all claims remaining in the application are respectfully requested.

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To the extent necessary, Applicants petition for an extension of time under 37 CFR § 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 501.40536X00) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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